Refine Search

Search Results -

Terms	Documents
L2 and ((without or "not") adj3 (signal$3 or inform$3))	8

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L3

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Monday, July 23, 2007 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L3	L2 and ((without or "not") adj3 (signal$3 or inform$3))	8	L3
L2	L1 same (queu$3 or buffer$3)	204	L2
L1	(transaction or task or job) near3 (reorder$3 or (re adj1 order$3))	624	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(709/100 |709/208 |710/110 |710/107 |710/263 |710/41 |710/52 |710/311 |718/100 |718/101 |718/102 |718/103 |718/104 |718/105 |718/106 |718/107 |718/108 |711/151 |714/47).ccls.	13445

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L4

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, July 23, 2007 Purge Queries Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	710/110,107,263,41,52,311;709/100,208;714/47;711/151;718/100-108.ccls.	13445	L4
L3	L2 and ((without or "not") adj3 (signal$3 or inform$3))	8	L3
L2	L1 same (queu$3 or buffer$3)	204	L2
L1	(transaction or task or job) near3 (reorder$3 or (re adj1 order$3))	624	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2 and L4	54

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L5

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, July 23, 2007 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L5	l2 and L4	54	L5
L4	710/110,107,263,41,52,311;709/100,208;714/47;711/151;718/100-108.ccls.	13445	L4
L3	L2 and ((without or "not") adj3 (signal$3 or inform$3))	8	L3
L2	L1 same (queu$3 or buffer$3)	204	L2
L1	(transaction or task or job) near3 (reorder$3 or (re adj1 order$3))	624	L1

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L1: (339) (transaction or ta
  - L2: (123) l1 same (queu$3 or
  - L3: (45) l2 same (slave or d
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search | List | Browse | Queue | Clear

DBs | USPAT | Plurals

Default operator: OR | Highlight all hit terms initially

BRS form | IS&R form | Image | Text | HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	339	(transaction or task or job) near3 (reorder$3 or	USPAT	2007/07/23 13:00			
2	BRS	L2	123	l1 same (queu$3 or buffer$3)	USPAT	2007/07/23 13:01			
3	BRS	L3	45	l2 same (slave or device or (I adj1 O) or (input	USPAT	2007/07/23 13:03			

Start | EAST - [Un...

EAST - [Untitled1:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L1: (339) (transaction or ta
  - L2: (123) l1 same (queu$3 or
  - L3: (45) l2 same (slave or d
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search | List | Browse | Queue | Clear

DBs | USPAT

Plurals

Default operator: OR

Highlight all hit terms initially

```
l2 same (slave or device or (I adj1 O) or (input adj1
output))
```

BRS form | IS&R form | Image | Text | HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	☐	☐	US 7181556 B2	20070220	17	Transaction request servicing mechanism	710/110	370/389; 709/230;
2	☐	☐	US 7162546 B2	20070109	5	Reordering unrelated transactions from an ordered	710/5	710/33; 710/36;
3	☐	☐	US 7139859 B2	20061121	10	Inter-queue ordering mechanism	710/306	710/311; 710/315
4	☐	☐	US 7103684 B2	20060905	18	Single-chip USB controller reading power-on boot code	710/62	710/20; 710/22;
5	☐	☐	US 7099986 B2	20060829	127	High speed peripheral interconnect apparatus,	710/314	710/306; 710/311

Start | EAST - [Un...

EAST - [Untitled1:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L1: (339) (transaction or ta
  - L2: (123) l1 same (queu$3 or
  - L3: (45) l2 same (slave or d
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search | List | Browse | Queue | Clear

DBs | USPAT | Plurals | Highlight all hit terms initially

Default operator: OR

```
l2 same (slave or device or (I adj1 O) or (input adj1
output))
```

BRS form | IS&R form | Image | Text | HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
12	☐	☐	US 6883045 B1	20050419	26	Apparatus for reordering graphics responses in a	710/52	710/20; 710/21;
13	☐	☐	US 6862647 B1	20050301	15	System and method for analyzing bus transactions	710/313	710/305; 710/306;
14	☐	☐	US 6857033 B1	20050215	16	I/O node for a computer system including an	710/62	710/2; 710/305;
15	☐	☐	US 6839784 B1	20050104	15	Control unit of an I/O node for a computer system	710/240	710/112; 710/113;
16	☐	☐	US 6834319 B1	20041221	15	Tunnel device for an input/output node of a	710/307	710/29; 710/313;

Start | EAST - [Un...

Results for "(((transaction or task or job)<in>metadata) <and> (reorder*<in>metadata)) an..." e-mail
Your search matched **74** of **1621473** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

View Session History

New Search

Modify Search

(((transaction or task or job)<in>metadata) <and> (reorder*<in>metadata)) and (q Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

view selected items Select All Deselect All View: 1-

1. **Efficient job scheduling in a mesh multicomputer without discrimination jobs**
Dugki Min; Mutka, M.W.;
Parallel and Distributed Processing, 1995. Proceedings. Seventh IEEE Sympo
25-28 Oct. 1995 Page(s):52 - 59
Digital Object Identifier 10.1109/SPDP.1995.530664
AbstractPlus | Full Text: PDF(592 KB) **IEEE CNF**
Rights and Permissions

2. **Improved task-allocation algorithms to maximize reliability of redundant computing systems**
Kartik, S.; Siva Ram Murthy, C.;
Reliability, IEEE Transactions on
Volume 44, Issue 4, Dec. 1995 Page(s):575 - 586
Digital Object Identifier 10.1109/24.475976
AbstractPlus | Full Text: PDF(820 KB) **IEEE JNL**
Rights and Permissions

3. **Task allocation algorithms for maximizing reliability of distributed compu**
Kartik, S.; Siva Ram Murthy, C.;
Computers, IEEE Transactions on
Volume 46, Issue 6, June 1997 Page(s):719 - 724
Digital Object Identifier 10.1109/12.600888
AbstractPlus | References | Full Text: PDF(100 KB) **IEEE JNL**
Rights and Permissions

4. **Multiversion cautious schedulers for database concurrency control**
Ibaraki, T.; Kameda, T.; Katoh, N.;
Software Engineering, IEEE Transactions on
Volume 16, Issue 3, March 1990 Page(s):302 - 315
Digital Object Identifier 10.1109/32.48938
AbstractPlus | Full Text: PDF(1280 KB) **IEEE JNL**
Rights and Permissions

5. **Impact of Job Mix on Optimizations for Space Sharing Schedulers**
Subhlok, J.; Gross, T.; Suzuoka, T.;
Supercomputing, 1996. Proceedings of the 1996 ACM/IEEE Conference on
1996 Page(s):54 - 54

AbstractPlus | Full Text: PDF(168 KB) **IEEE CNF**
Rights and Permissions

6. **A study of dynamic scheduling techniques for multiscalar processors**
Madavarapu, V.K.; Franklin, M.; Sundararaman, K.K.;
High Performance Computing, 1996. Proceedings. 3rd International Conferenc
19-22 Dec. 1996 Page(s):413 - 418
Digital Object Identifier 10.1109/HIPC.1996.565856
AbstractPlus | Full Text: PDF(468 KB) **IEEE CNF**
Rights and Permissions

7. **Solving dynamic tardiness problems in single machine environments**
Lasso, M.; Pandolfi, D.; De San Pedro, M.E.; Villagra, A.; Gallard, R.;
Evolutionary Computation, 2004. CEC2004. Congress on
Volume 1, 19-23 June 2004 Page(s):1143 - 1149 Vol.1
Digital Object Identifier 10.1109/CEC.2004.1330990
AbstractPlus | Full Text: PDF(490 KB) **IEEE CNF**
Rights and Permissions

8. **A certification protocol with low space overhead**
Sung Ho Cho; Kyoung Y. Bae; Chong-Sun Hwang;
Parallel and Distributed Systems, 1998. Proceedings., 1998 International Conf
14-16 Dec. 1998 Page(s):67 - 74
Digital Object Identifier 10.1109/ICPADS.1998.741021
AbstractPlus | Full Text: PDF(140 KB) **IEEE CNF**
Rights and Permissions

9. **LOMARC: Lookahead Matchmaking for Multiresource Coscheduling on H CPUs**
Sodan, A.C.; Lei Lan;
Parallel and Distributed Systems, IEEE Transactions on
Volume 17, Issue 11, Nov. 2006 Page(s):1360 - 1375
Digital Object Identifier 10.1109/TPDS.2006.160
AbstractPlus | Full Text: PDF(6007 KB) **IEEE JNL**
Rights and Permissions

10. **A Master-Slave Adaptive Load-Distribution Processor Model on PCA**
Ito, T.; Kitamichi, J.; Kuroda, K.; Okuyama, Y.;
Parallel and Distributed Processing Symposium, 2005. Proceedings. 19th IEEI
04-08 April 2005 Page(s):153a - 153a
Digital Object Identifier 10.1109/IPDPS.2005.42
AbstractPlus | Full Text: PDF(184 KB) **IEEE CNF**
Rights and Permissions

11. **High fan-in dynamic CMOS comparators with low transistor count**
Chua-Chin Wang; Po-Ming Lee; Chi-Feng Wu; Hsin-Long Wu;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti
Circuits and Systems I: Regular Papers, IEEE Transactions on]
Volume 50, Issue 9, Sept. 2003 Page(s):1216 - 1220
Digital Object Identifier 10.1109/TCSI.2003.816338
AbstractPlus | References | Full Text: PDF(453 KB) **IEEE JNL**
Rights and Permissions

12. **High-speed and low-power split-radix FFT**
Wen-Chang Yeh; Chein-Wei Jen;
Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Si
IEEE Transactions on]
Volume 51, Issue 3, March 2003 Page(s):864 - 874
Digital Object Identifier 10.1109/TSP.2002.806904

AbstractPlus | References | Full Text: PDF(787 KB) **IEEE JNL**
Rights and Permissions

☐ **13. Microprocessor specification in Hawk**
Matthews, J.; Cook, B.; Launchbury, J.;
Computer Languages, 1998. Proceedings. 1998 International Conference on
14-16 May 1998 Page(s):90 - 101
Digital Object Identifier 10.1109/ICCL.1998.674160

AbstractPlus | Full Text: PDF(236 KB) **IEEE CNF**
Rights and Permissions

☐ **14. The HP PA-8000 RISC CPU**
Kumar, A.;
Micro, IEEE
Volume 17, Issue 2, March-April 1997 Page(s):27 - 32
Digital Object Identifier 10.1109/40.592310

AbstractPlus | Full Text: PDF(128 KB) **IEEE JNL**
Rights and Permissions

☐ **15. In-line interrupt handling and lock-up free translation lookaside buffers (**
Jaleel, A.; Jacob, B.;
Computers, IEEE Transactions on
Volume 55, Issue 5, May 2006 Page(s):559 - 574
Digital Object Identifier 10.1109/TC.2006.77

AbstractPlus | Full Text: PDF(5072 KB) **IEEE JNL**
Rights and Permissions

☐ **16. Dynamic resizing of superscalar datapath components for energy efficie**
Ponomarev, D.; Kucuk, G.; Ghose, K.;
Computers, IEEE Transactions on
Volume 55, Issue 2, Feb. 2006 Page(s):199 - 213
Digital Object Identifier 10.1109/TC.2006.23

AbstractPlus | Full Text: PDF(1568 KB) **IEEE JNL**
Rights and Permissions

☐ **17. Energy efficient comparators for superscalar datapaths**
Ponomarev, D.V.; Kucuk, G.; Ergin, O.; Ghose, K.;
Computers, IEEE Transactions on
Volume 53, Issue 7, July 2004 Page(s):892 - 904
Digital Object Identifier 10.1109/TC.2004.29

AbstractPlus | References | Full Text: PDF(1112 KB) **IEEE JNL**
Rights and Permissions

☐ **18. Isolating short-lived operands for energy reduction**
Ponomarev, D.; Kucuk, G.; Ergin, O.; Ghose, K.;
Computers, IEEE Transactions on
Volume 53, Issue 6, June 2004 Page(s):697 - 709
Digital Object Identifier 10.1109/TC.2004.11

AbstractPlus | References | Full Text: PDF(1456 KB) **IEEE JNL**
Rights and Permissions

☐ **19. Complexity-effective reorder buffer designs for superscalar processors**
Kucuk, G.; Ponomarev, D.V.; Ergin, O.; Ghose, K.;
Computers, IEEE Transactions on
Volume 53, Issue 6, June 2004 Page(s):653 - 665
Digital Object Identifier 10.1109/TC.2004.5

AbstractPlus | References | Full Text: PDF(1376 KB) **IEEE JNL**
Rights and Permissions

☐ **20. A novel reordering write buffer to improve write performance of log-struc systems**
Jun Wang; Yiming Hu;
Computers, IEEE Transactions on
Volume 52, Issue 12, Dec. 2003 Page(s):1559 - 1572
Digital Object Identifier 10.1109/TC.2003.1252852
AbstractPlus | References | Full Text: PDF(2135 KB) IEEE JNL
Rights and Permissions

☐ **21. Composable proxy services to support collaboration on the mobile Interi**
McKinley, P.K.; Padmanabhan, U.I.; Ancha, N.; Sadjadi, S.M.;
Computers, IEEE Transactions on
Volume 52, Issue 6, June 2003 Page(s):713 - 726
Digital Object Identifier 10.1109/TC.2003.1204828
AbstractPlus | References | Full Text: PDF(4141 KB) IEEE JNL
Rights and Permissions

☐ **22. Improving computer architecture simulation methodology by adding stat**
Yi, J.J.; Lilja, D.J.; Hawkins, D.M.;
Computers, IEEE Transactions on
Volume 54, Issue 11, Nov. 2005 Page(s):1360 - 1373
Digital Object Identifier 10.1109/TC.2005.184
AbstractPlus | Full Text: PDF(2184 KB) IEEE JNL
Rights and Permissions

☐ **23. Lookahead scheduling requests for multisize page caching**
Kiniwa, J.; Hamada, T.; Mizoguchi, D.;
Computers, IEEE Transactions on
Volume 50, Issue 9, Sept. 2001 Page(s):972 - 983
Digital Object Identifier 10.1109/12.954511
AbstractPlus | References | Full Text: PDF(488 KB) IEEE JNL
Rights and Permissions

☐ **24. Designing a modern memory hierarchy with hardware prefetching**
Wei-Fen Lin; Reinhardt, S.K.; Burger, D.;
Computers, IEEE Transactions on
Volume 50, Issue 11, Nov. 2001 Page(s):1202 - 1218
Digital Object Identifier 10.1109/12.966495
AbstractPlus | References | Full Text: PDF(1732 KB) IEEE JNL
Rights and Permissions

☐ **25. Dynamic access ordering for streamed computations**
McKee, S.A.; Wulf, W.A.; Aylor, J.H.; Klenke, R.H.; Salinas, M.H.; Hong, S.I.; V
Computers, IEEE Transactions on
Volume 49, Issue 11, Nov. 2000 Page(s):1255 - 1271
Digital Object Identifier 10.1109/12.895941
AbstractPlus | References | Full Text: PDF(360 KB) IEEE JNL
Rights and Permissions

View: 1-

Efficient job scheduling in a mesh multicomputer without discrimination against large jobs

Dugki Min Mutka, M.W.
Dept. of Comput. Sci., Kon-Kuk Univ., Seoul, South Korea;

This paper appears in: **Parallel and Distributed Processing, 1995. Proceedings. Seventh IEEE Symposium on**
Publication Date: 25-28 Oct. 1995
On page(s): 52 - 59
Meeting Date: 10/25/1995 - 10/28/1995
Location: San Antonio, TX
INSPEC Accession Number:5119665
Digital Object Identifier: 10.1109/SPDP.1995.530664
Posted online: 2002-08-06 20:08:33.0

Abstract
Many innovative schemes for allocating **jobs** to parallel computing systems have been proposed in order to achieve highly utilized parallel computing systems. The schemes have tried to achieve good **job** response times with little system fragmentation of **processing** resources. Since most schemes have concentrated on approaches for **processor** allocation, the schemes have used First-Come-First-Serve (FCFS) as the **job** scheduling discipline. However, it has been previously established that **job** scheduling algorithms for parallel computing systems can have a large impact on the system utilization and **job** response time. Schemes that use multiple **queues**, which **reorder** the sequence of **jobs** allocated to the parallel system, can be very effective in improving the system performance. However, such non-FCFS schemes have been criticized because they provide improved average performance by favoring small **jobs** at the expense of large jobs. In order to achieve improved performance by means of multiple **queue job** scheduling schemes without sacrificing the fairness of FCFS, we propose a new **job** scheduling discipline that behaves in a FCFS manner under low loaded conditions, but exploits performance enhancing features of multiple **queue** schemes under highly loaded conditions. In addition, the scheme does not inappropriately discriminate against large **jobs**

Index Terms
Inspec
Controlled Indexing
multiprocessing systems performance evaluation **processor** scheduling resource allocation scheduling

Non-controlled Indexing

First-Come-First-Serve discrimination **job** allocation **job** response time **job** response times **job** scheduling **job** scheduling algorithms large **jobs** mesh multicomputer multiple **queue job** scheduling schemes multiple **queues** parallel computing systems **processor** allocation small **jobs** system fragmentation system performance system utilization

Author Keywords
Not Available

References

No references available on IEEE Xplore.

Citing Documents

No citing documents available on IEEE Xplore.

◂ View Search Results | Next Article ▸